

compositor 6 for compositing scrambled data supplied from a first scrambler and scrambled data supplied from a second scrambler, a phase modulator 7 for phase-modulating composited data supplied from the channel compositor 6, a SYNC signal adding and correcting unit 8 for receiving SYNC timing signals generated at and supplied from a SYNC timing generator 9 and inserting SYNC signals into phase-modulated one-bit audio signal data supplied from the phase modulator 7 to generate SYNC patterns and correct the SYNC patterns, and an information data adding unit 11 for adding information data which is related with one-bit audio signals to phase-modulated one-bit audio signal data via the SYNC signal adding and correcting unit 8 by rearranging data of inverted phases thereof based on a two channel unit.--

IN THE CLAIMS

Please amend claims 1, 3-7 and 9-13 by rewriting same to read as follows:

--1. (Amended) (Amended) A digital signal encoding apparatus for encoding one-bit signals of a plurality of n channels, n being equal to at least two, and the one-bit signals being modulated in a delta-sigma manner, the apparatus comprising:

phase modulating means for phase-modulating the one-bit signals as original signals to add data of inverted phases to the one-bit signals; and